

<b>Notice of References Cited</b>	Application/Control No. 10/806,612	Applicant(s)/Patent Under Reexamination NAHAS, JOSEPH J.	
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**U.S. PATENT DOCUMENTS**

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	U	Kim et al., Macro Model and Sense Amplifier for a MRAM, December 2002, Journal of the Korean Physical Society, Vol. 41, No. 6, pp. 896-901 ✓
	V	Bodhisattva Das and William C. Black, A Generalized HSPICE Macro-Model for Pinned Spin-Dependent-Tunneling Devices, September 1999, IEEE Transactions On Magnetism, Vol. 35, No. 5, pages 2889-2891 ✓
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	U	Theodoros Dimopoulos, Transport Polarisé En Spin Dans Les Jontions Tunnel Magnétiques: Le Rôle Des Interfaces Métal/Oxyde Dans Le Processus Tunnel, Jan 11 '02, Thesis, Institut de Physique et Chimie des Matériaux de Strasbourg, Pp 146, 152-154, 160, 161 ✓
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